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CONFIRMATION NO. 6890

22850  
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 1940 DUKE STREET  
 ALEXANDRIA, VA 22314

## CORRECTED FILING RECEIPT



\*OC000000015534857\*

Date Mailed: 03/22/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Hiroaki Hazama, Tokyo, JAPAN;

## Assignment For Published Patent Application

Kabushiki Kaisha Toshiba, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 22850.

Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2002-322476 11/06/2002

If Required, Foreign Filing License Granted: 11/08/2003

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/639,443**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

**Title**

A SEMICONDUCTOR MEMORY DEVICE HAVING A MULTI-LAYERED INTERLAYER  
INSULATION CONSISTING OF DEUTERIUM AND NITRIDE

**Preliminary Class**

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